**委托检测申请书**

 委托书编号：

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| 客户填写 | 委托单位名称 |  | 联 系 人 |  | 联系电话 |  |
| 委托单位地址 |  | 样品数量 |  | E-mail |  |
| 样品名称/型号 |  | 对应合同/报价单号 |  |
| 样品处理意见 | □退样 □委托我司处置 | 报告数据发放形式 | □自取 □快递 |
| （高速接口）检测项目 | □1.HDMI1.4b Cable □2.HDMI1.4b Source □3.HDMI1.4b Sink □4.HDMI1.4b Repeater □5.HDMI2.0 Source□6.HDMI2.0 Sink □7.HDMI2.0 Repeater □8.HDMI 1.4b Directly Attached Source □9.HDMI 1.4b Directly Attached Sink□10.其它  |
| 特殊说明：如果您是电子填单，以上请于相应位置复制粘贴：🗹 |
| 检测标准 | 具体检测标准及检测子项目，请勾选后续附件： （附件序号）。 |
| 备注 |  |
| 测试周期 |  个工作日 | 检测费用 |  元 |
| 说明：1. 测试周期以我司收到样品之日起计算。除另有特别约定，自检测报告发出日算起，本公司对送检的样品只保留3个月。
2. 委托方寄出样品时请保证样品包装完好，配件齐全，能正常工作，以便检测工作展开。
3. 检测标准请见附件，如无特殊说明或要求，以下检测标准均为有效现行版本。
4. 异议复议期为自委托方收到检验检测报告日算起7天以内，逾期不受理。
5. 如有更多疑问，请联系雷群群小姐，联系电话：0755-61363380 ，同时报出您的委托书编号（该表右上角）。
 |
| 委托人： 日期： | 受理人： 日期： |

附件1：HDMI 1.4b Cable兼容性检测

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| 检测子项目 | 依据标准 |
| □ 5-1:Connector Maximum Envelope□ 5-2:Wire Assignment□ 5-3:TMDS Data Eye Diagram□ 5-4:Intra-Pair Skew□ 5-5:Inter-Pair Skew□ 5-6:Far End Crosstalk□ 5-7:Attenuation and Phase□ 5-8:Differential Impedance□ 5-10:DDC/CEC Line Capacitance and Voltage | □ 5-11:+5V Power□ 5-12:HPD Signal□ 5-13:DDC Communication□ 5-14:CEC Communication□ 5-15:Utility Line Impedance□ 5-16:Type E Cable Wire Thermal Deformation (ISO 6722)□ HEACT 6-1：Intra-Pair Skew Test□ HEACT 6-2：Differential Attenuation Test□ HEACT 6-3：Differential/Common mode Impedance Test | □High-Definition Multimedia Interface Compliance Test Specification Version 1.4b□其它：  |

附件2：HDMI 1.4b Source兼容性检测

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| 检测子项目 | 依据标准 |
| □ 7-1:EDID-Related Behavior□ 7-2:TMDS –VL□ 7-3:TMDS – VOFF□ 7-4:TMDS – TRISE, TFALL□ 7-6:TMDS – Inter-Pair Skew□ 7-7:TMDS – Intra-Pair Skew □ 7-8:TMDS – Clock Duty Cycle □ 7-9:TMDS – Clock Jitter □ 7-10:TMDS – Data Eye Diagram □ 7-11:+5V Power □ 7-12:Hot Plug Detect □ 7-13:DDC/CEC Line Capacitance and Voltage □ 7-14:CEC Line Connectivity □ 7-15:CEC Line Degradation □ 7-16:Legal Codes □ 7-17:Basic Protocol □ 7-18:Extended Control Period □ 7-19:Packet Types□ 7-21:Minimum Format Support□ 7-22:Additional Format Support | □ 7-23:Pixel Encoding - RGB to RGB-only Sink□ 7-24:Pixel Encoding - YCBCR to YCBCR Sink□ 7-25:Video Format Timing□ 7-26:Pixel Repetition□ 7-27:AVI InfoFrame□ 7-28:IEC 60958 /IEC 61937□ 7-29:ACR□ 7-30:Audio Sample Packet Jitter□ 7-31:Audio InfoFrame□ 7-32:Audio Sample Packet Layout□ 7-33:Interoperability With DVI□ 7-34:Deep Color□ 7-35:Gamut Metadata Transmission□ 7-36:High-Bitrate Audio□ 7-37:One Bit Audio□ 7-38:3D Video Format Timing□ 7-39:4K x 2K Video Format Timing□ 7-40:Extended Colorimetry Transmission (without xvYCC) | □ High-Definition Multimedia Interface Compliance Test Specification Version 1.4b□ 其它：  |

附件3：HDMI 1.4b Sink兼容性检测

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| 检测子项目 | 依据标准 |
| □ 8-1:EDID Readable□ 8-2:EDID VESA Structure□ 8-3:CEA Timing Extension Structure□ 8-4:TMDS Termination Voltage□ 8-5:TMDS Minimum Differential Sensitivity□ 8-6:TMDS Intra-Pair Skew□ 8-7:TMDS Jitter Tolerance□ 8-8:TMDS Differential Impedance□ 8-9:DDC/CEC Line Capacitance and Voltage □ 8-10:HPD Output Voltage □ 8-11:HPD Output Resistance □ 8-12:+5V Power Max Current □ 8-13:CEC Line Connectivity □ 8-14:CEC Line Degradation □ 8-15:Character Synchronization | □ 8-16:Acceptance of All Valid Packet Types□ 8-17:Basic Format Support Requirements□ 8-18:HDMI Format Support Requirements□ 8-19:Pixel Encoding Requirements□ 8-20:Video Format Timing□ 8-21:Audio Clock Regeneration□ 8-22:Audio Sample Packet Jitter□ 8-23:Audio Formats□ 8-24:Interoperability with DVI□ 8-25:Deep Color□ 8-27:High Bitrate Audio□ 8-28:One Bit Audio□ 8-29:3D Video Format Timing□ 8-30:4K x 2K Video Format Timing□ 8-31:AVI InfoFrame supporting Extended Colorimetry, Content Type and Selectable YCC Quantization Range | □ High-Definition Multimedia Interface Compliance Test Specification Version 1.4b□其他：  |

附件4：HDMI 1.4b Repeater兼容性检测

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| 检测子项目 | 依据标准 |
| □ 9-1: Repeated Output Port□ 9-2: Source Functionality□ 9-3: Repeated Input Port | □ 9-4: Sink Functionality□ 9-5: Physical Address | □ High-Definition Multimedia Interface Compliance Test Specification Version 1.4b□其他：  |

附件5：HDMI 2.0 Source兼容性检测

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| 检测子项目 | 依据标准 |
| □HF1-1: Source TMDS Electrical–6G–VL and Vswing□HF1-2: Source TMDS Electrical–6G–TRISE, TFALL□HF1-3: Source TMDS Electrical–6G–Inter-Pair Skew□HF1-4: Source TMDS Electrical–6G–Intra-Pair Skew□HF1-5: Source TMDS Electrical–6G–Differential Voltage□HF1-6: Source TMDS Electrical–6G–Clock Duty Cycle and Clock Rate□HF1-7: Source TMDS Electrical–6G–Clock Jitter□HF1-8: Source TMDS Electrical–6G–Data Eye Diagram□HF1-9: Source TMDS Electrical–6G–Differential Impedance□HF1-10: Source TMDS Protocol–6G–TMDS Bit Clock Ratio□HF1-11: Source TMDS Protocol–6G–2160p Legal Codes□HF1-12: Source TMDS Protocol–6G–Basic Protocol and Scrambling□HF1-13: Source TMDS Protocol–Scrambling ≤ 3.4Gbps□HF1-14: Source Video Timing–6G–2160p 24-bit Color Depth□HF1-15: Source Video Timing–6G–2160p Deep Color□HF1-16: Source Video Timing–6G–2160p 3D□HF1-18: Source AVI InfoFrame and GCP–6G–2160p□HF1-21: Source TMDS Protocol – 6G – Non-2160p Legal Codes□HF1-22: Source TMDS Protocol–6G–Non-2160p Basic Protocol and Scrambling | □HF1-24: Source Video Timing–6G–Non-2160p 24-bit Color Depth□HF1-25: Source Video Timing–6G–Non-2160p Deep Color□HF1-26: Source Video Timing–6G–Non-2160p 3D□HF1-28: Source AVI InfoFrame and GCP–6G–Non-2160p□HF1-31: Source Pixel Encoding–YCBCR 4:2:0–TMDS Pixel Encoding□HF1-32: Source Pixel Encoding–YCBCR 4:2:0 Deep Color–TMDS Pixel Encoding□HF1-33: Source Video Timing–YCBCR 4:2:0□HF1-34: Source Video Timing–YCBCR 4:2:0 Deep Color□HF1-35: Source Video Timing–21:9 (64:27)□HF1-41: Source Audio Encoding–3D Audio–IEC Sample Packet□HF1-43: Source Audio Encoding–HBR Audio–IEC Audio Stream Packet□HF1-44: Source Audio InfoFrame–3D and MS Audio–Supported Frequency□HF1-45: Source Audio Channel Status–Basic Audio–Allowed Rate□HF1-47: Source HDMI-VSIFs–3D OSD Disparity□HF1-48: Source HDMI-VSIFs–Dual-View□HF1-49: Source HDMI-VSIFs–Independent-View□HF1-51: Source AVI InfoFrame–YCBCR 4:2:0□HF1-52: Source AVI InfoFrame and GCP–YCBCR 4:2:0 BT.2020□HF1-53: Source Dynamic Range and Mastering InfoFrame – High Dynamic Range | □ High-Definition Multimedia Interface Compliance Test Specification Version 2.0s□ 其它：  |

附件6：HDMI 2.0 Sink兼容性检测

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| 检测子项目 | 依据标准 |
| □HF2-1: Sink TMDS Electrical – 6G –Min/Max Differential Swing Tolerance□HF2-2: Sink TMDS Electrical – 6G – Intra-Pair Skew□HF2-3: Sink TMDS Electrical – 6G – Jitter Tolerance□HF2-4: Sink TMDS Electrical – 6G – Differential Impedance□HF2-5: Sink TMDS Protocol – 6G – Scrambling□HF2-6: Sink Video Timing – 6G – 2160p 24-bit Color Depth□HF2-7: Sink Video Timing – 6G – 2160p Deep Color□HF2-8: Sink Video Timing – 6G – 2160p 3D□HF2-9: Sink TMDS Protocol – Scrambling ≤ 340Mcsc□HF2-10: Sink Video Timing – 6G – HF-VSDB□HF2-12 Sink E-DDC – Read Request – Enable Verification□HF2-16 Sink E-DDC – Read Request – Test Configuration Register Reset□HF2-23: Sink Pixel Decoding – YCBCR 4:2:0□HF2-24: Sink Pixel Decoding – YCBCR 4:2:0 Deep Color□HF2-25: Sink Video Timing – 21:9 (64:27)□HF2-26: Sink EDID – Video Format Declaration□HF2-30: Sink Audio Decoding and Rendering – Multi-stream Audio (L-PCM and 61937) –Sample Packet | □HF2-31: Sink EDID – YCBCR 4:2:0 - Data Blocks□HF2-32: Sink EDID – YCBCR 4:2:0 BT.2020 – Data Block□HF2-34: Sink E-DDC – Read Request – SCDC Update Flag Response□HF2-35: Sink EDID – YCBCR 4:2:0 Deep Color HF-VSDB□HF2-36: Sink Video Timing – 6G – Non-2160p 24-bit Color Depth□HF2-37: Sink Video Timing – 6G – Non-2160p Deep Color□HF2-38: Sink Video Timing – 6G – Non-2160p 3D□HF2-39: Sink EDID – 3D and Multi-stream Audio Data Blocks□HF2-40: Sink HDMI-VSIFs – Dual-View□HF2-41: Sink EDID – HDMI-VSDBs – Independent-View□HF2-42: Sink A/V Relationship – DALS□HF2-43: Sink HDMI-VSIFs – 3D OSD Disparity□HF2-44: Sink E-DDC – Read Request – SCDC Wait For Buss Free□HF2-50: Sink E-DDC – Read Request – TestReadRequest and TestReadRequestDelay□HF2-53: Sink EDID – HF-VSDB□HF2-54: Sink EDID – HDR Static Metadata Data Block | □ High-Definition Multimedia Interface Compliance Test Specification Version 2.0s□ 其它：  |

附件7：HDMI 2.0 Repeater兼容性检测

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| 检测子项目 | 依据标准 |
| □ HF3-1: Repeater Repeated Output Port – 6G – 2160p □HF3-2: Repeater Repeated Output Port – 6G – Source Functionality 2160p□ HF3-3: Repeater Repeated Input Port – 6G – 2160p□HF3-4: Repeater Repeated Input Port – 6G – Sink Functionality 2160p | □ HF3-21: Repeater Repeated Output Port HDR□HF3-22: Repeater Repeated Output Port Source Functionality HDR□ HF3-23: Repeater Repeated Input Port HDR□ HF3-24: Repeater Repeated Input Port Sink Functionality HDR | □ High-Definition Multimedia Interface Compliance Test Specification Version 2.0s□ 其它：  |

附件8：HDMI 1.4b Directly Attached Source兼容性检测

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| 检测子项目 | 依据标准 |
| □ 5-1: Connector Maximum Envelope□ 5-3:TMDS Data Eye Diagram□ 5-4:Intra-Pair Skew□ 5-5:Inter-Pair Skew□ 5-8:Differential Impedance□ 5-10:DDC/CEC Line Capacitance and Voltage□ 5-15:Utility Line Impedance□ 7-1:EDID-Related Behavior□ 7-3:TMDS – VOFF□ 7-8:TMDS – Clock Duty Cycle □ 7-9:TMDS – Clock Jitter □ 7-11:+5V Power □ 7-12:Hot Plug Detect □ 7-16:Legal Codes □ 7-17:Basic Protocol □ 7-18:Extended Control Period □ 7-19:Packet Types□ 7-21:Minimum Format Support□ 7-22:Additional Format Support | □ 7-23:Pixel Encoding - RGB to RGB-only Sink□ 7-24:Pixel Encoding - YCBCR to YCBCR Sink□ 7-25:Video Format Timing□ 7-26:Pixel Repetition□ 7-27:AVI InfoFrame□ 7-28:IEC 60958 /IEC 61937□ 7-29:ACR□ 7-30:Audio Sample Packet Jitter□ 7-31:Audio InfoFrame□ 7-32:Audio Sample Packet Layout□ 7-33:Interoperability With DVI□ 7-34:Deep Color□ 7-35:Gamut Metadata Transmission□ 7-36:High-Bitrate Audio□ 7-37:One Bit Audio□ 7-38:3D Video Format Timing□ 7-39:4K x 2K Video Format Timing□7-40:Extended Colorimetry Transmission (without xvYCC) | * High-Definition Multimedia Interface Compliance Test Specification Version 1.4b
* Implementation Guideline for Directly Attached Device Version 1.0
* 其它：
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附件9：HDMI 1.4b Directly Attached Sink兼容性检测

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| 检测子项目 | 依据标准 |
| □ 5-1:Connector Maximum Envelope□ 5-3:TMDS Data Eye Diagram□ 5-8:Differential Impedance□ 5-10:DDC/CEC Line Capacitance and Voltage □ 8-1:EDID Readable□ 8-2:EDID VESA Structure□ 8-3:CEA Timing Extension Structure□ 8-4:TMDS Termination Voltage□ 8-10:HPD Output Voltage □ 8-11:HPD Output Resistance □ 8-12:+5V Power Max Current □ 8-14:CEC Line Degradation □ 8-15:Character Synchronization□ 8-16:Acceptance of All Valid Packet Types | □ 8-17:Basic Format Support Requirements□ 8-18:HDMI Format Support Requirements□ 8-19:Pixel Encoding Requirements□ 8-20:Video Format Timing□ 8-21:Audio Clock Regeneration□ 8-22:Audio Sample Packet Jitter□ 8-23:Audio Formats□ 8-24:Interoperability with DVI□ 8-25:Deep Color□ 8-27:High Bitrate Audio□ 8-28:One Bit Audio□ 8-29:3D Video Format Timing□ 8-30:4K x 2K Video Format Timing□ 8-31:AVI InfoFrame supporting Extended Colorimetry, Content Type and Selectable YCC Quantization Range | * High-Definition Multimedia Interface Compliance Test Specification Version 1.4b
* Implementation Guideline for Directly Attached Device Version 1.0
* 其它：
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附件10：其它项目

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| 检测子项目 | 依据标准 |
| □  | □  |